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S Kim, AK Soman - Dependable Systems and Networks, 2002. DSN 2002. Proceedings ..., 2002 - ieeexplore.ieee.org
... the Sun community source licensing program [32]. ... stack cache (SC), and trap generation/dependencychecking/forwarding ... a 16-KB direct-mapped **instruction** cache (...

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Partitioning for model-based design

H Taitel - US Patent App. 11/894,695. 2007 - Google Patents
... to one aspect of the invention, a method includes **identifying** portions of a ... **Generating** includes applying a set of software **instructions** resident in the ...

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Method and device for automatically generating simulation programs

L Heidemann, H Seybold - 2002 - freepatentsonline.com
... [0008] The **simulation model or program** can advantageously ... The reactor contains two **generating** sets, a mixer ... for example, comprise the **instructions**: Check status ...
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Maintenance subsystem for computer network including power control and remote diagnostic center

DA Andreasen, JH Armstrong, JE Buggert, HK Desai, ... - US Patent 4,695,946. 1987 - Google Patents
... **CLOCK • TRANSMIT CLOCK • DPLL CLOCK • DR GENERATOR CLOCK 200 ... FIG.6. PR'QR ART CIO PORT C BLOCK DIAGRAM ... DATA 620 MULTIPLEXER 613- AA w PORT CONTROL LOGIC 618 ...**
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Method, apparatus, and program storage device for sharing objects with a network server and a ...

DE House, BJ Owings, SS Pai - US Patent 5,890,158. 1999 - Google Patents
... FROM THE APPENDED COMMON FILE 518 **GENERATING** A SECOND ... Moreover, the computer **programs** and operating system are comprised of **instructions** which, when ...
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Simulating a multi-tiered computer environment on a single development system for debugging

DE House, CJ Nelin - US Patent 5,940,593. 1999 - Google Patents
... the development computer, a means for **generating** an APP ... Moreover, the computer **programs** and operating system are comprised of **instructions** which, when ...
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The discipline of embedded systems design- ►KU.edu (PDF)

TA Henzinger, J Sifakis, L EPFL - Computer, 2007 - ieeexplore.ieee.org
... migrate blocks, which can represent **instructions**, subroutines, or ... error loop of code **generation**, followed by ... hardware and for synchronous real-time software. ...
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Minimization of blocking time in component-based software architecture for control systems

Y Jeon, CH Choi - Control Engineering Practice, 2001 - Elsevier
... handle user interaction while **generating** trajectories and ... as dedicated processors for time-critical tasks, but ... be thought of as software **components** though no ...
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Energy-focused re-compilation of executables and hardware mechanisms based on compiler-architecture ...

S Chheda, K Carver, R Ashok - US Patent App 10/967,989, 2004 - Google Patents
... fi * 42 Energy Aware Binary **Generator** * * i ^ ^ 7 ... transformation tech- niques to
different instruction sets ... to help perform sophisticated **program** analyses and ...

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Design aids for VLSI: The Berkeley perspective

A Newton, D Pederson, A Sangiovanni-Vincentelli, C ... - IEEE Transactions on Circuits and Systems, 1981 -
ieeexplore.ieee.org

... of integrated circuits increases, the **generation**, management, and ... the designer nor
the design software to produce ... the circuit and its **component** parts wherever ...

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Key authors: **D Andreasen - J Buggert - H Desai - Z Hussain - S Kim**

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